

Greenbank Electronics

OUR REF: AN-C22/3

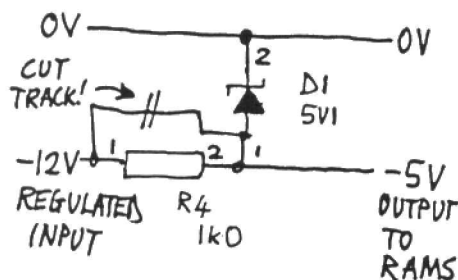
6/10/80

USE OF MXD-2 16K DYNAMIC RAM BOARD ON 'ISBUS' SYSTEMS

WE ARE IN THE COURSE OF PREPARING SOME ADDITIONAL NOTES OF OUR OWN DESCRIBING THE KEMITRON MXD-2 BOARD, WITH PARTICULAR REFERENCE TO ITS USE IN 'ISBUS' SYSTEMS, AND THESE DETAILS WILL BE SENT TO ALL PURCHASERS IN DUE COURSE.

IN THE MEANTIME WE ARE SENDING A DRAFT COPY OF THE COMPONENT LAYOUT WE WILL BE PROPOSING.

AS THE BOARD REQUIRES -5V, WHICH IS UNAVAILABLE ON THE STANDARD 'ISBUS', WE SUGGEST THAT THIS BE DERIVED FROM THE -12V BY MEANS OF THE FOLLOWING CIRCUIT



THE ZENER DIODE IS FITTED IN PLACE OF ONE OF THE 100nF DISC CERAMIC CAPACITORS.

ITS POSITION, AND THAT OF R4, CAN BE IDENTIFIED FROM THE DRAFT COPY OF THE MXD-2 ASSEMBLY DRAWING BUT NOTE THAT THE TRACK BENEATH R4 MUST BE CUT BEFORE IT IS FITTED.

D1 AND R4 MAY BE LEFT UNUSED IF A -5V SUPPLY IS AVAILABLE, AND IF 'ISBUS' COMPATABILITY IS OF NO IMPORTANCE.

A DETAILED TIMING DIAGRAM WILL BE INCLUDED IN THE ADDITIONAL NOTES WE ARE PREPARING, BUT FOR THE MOMENT WE WOULD SUGGEST THAT STANDARD OR LOW-POWER SCHOTTKY ('LS') TTL BE FITTED AND THE CPU CLOCK BE FIXED AT 2.0 MHz. EXPERIENCE SHOWS THAT 4.0 MHz OPERATION IS PERFECTLY POSSIBLE (SO FAR IN 100% OF CASES), BUT WE PERSONALLY WOULD NOT WISH TO GUARANTEE THIS.

D. M.P. 6-10-80